What is claimed is:

1. A data stream processing system of Direct Stream Digital (DSD) technology, for converting each original digital data streams processed by DSD into corresponding analog signals, the system comprising:

    a combining module, for combining two original digital data streams into one data stream based on a combining rule, then generating a plurality of data streams;

    a clock generating module, for generating a first clock signal and a second clock signal, the first clock signal being used for defining a predetermined converting period of each data stream, and the second clock signal is used for triggering a converting function of each digital data of each data stream; and

    a digital-to-analog converter (DAC), for converting each digital data of each data streams into a corresponding analog signal based on the first and the second clock signals, then generating a plurality of analog signals.

2. The data stream processing system of claim 1, wherein the combining rule is to chose two original digital data streams from a plurality of original digital data streams.

3. The data stream processing system of claim 1, wherein the first clock signal has a positive edge and a negative edge in each predetermined converting period.

4. The data stream processing system of claim 3, wherein the positive edge triggers the DAC converting the digital data of a first original digital data stream of each data streams.

5. The data stream processing system of claim 4, wherein the negative edge triggers the DAC converting the digital data of a second original digital data stream of

each data streams.

6. A data stream transmission interface of DSD technology, for transmitting data between a decoder and a DAC, the data stream transmission interface comprising:

at least one data stream transmission pin, for transmitting at least one data stream from the decoder to the DAC, the data stream comprising two different original digital data streams arranged by a predetermined interval;

a first clock signal transmission pin, for transmitting a first clock signal, the first clock signal defining a predetermined converting period for the data stream according to the predetermined interval;

a second clock signal transmission pin, for transmitting a second clock signal, the second clock signal being used for triggering the DAC converting each digital data of the data stream to a corresponding analog signal.

7. The data stream transmission interface of claim 6, wherein the decoder decodes a coding digital signal comprising a plurality of original digital data streams to generate the data stream.

8. The data stream transmission interface of claim 6, wherein the first clock signal has a positive edge and a negative edge in each predetermined converting period.

9. The data stream transmission interface of claim 8, wherein the positive edge triggers the DAC converting the digital data of a first original digital data stream.

10. The data stream transmission interface of claim 8, wherein the negative edge triggers the DAC converting the digital data of a second original digital data stream.

11. A data stream processing method of Direct Stream Digital (DSD) technology for

converting each original digital data streams processed by DSD into corresponding analog signals, the method comprising:

combining two original digital data streams into one data stream based on a combining rule, then generating a plurality of data streams;

generating a first clock signal and a second clock signal, the first clock signal being used for defining a predetermined converting period of each data streams, and the second clock signal being used for triggering a converting process for each digital data of each data streams; and

converting each digital data of each data streams into a corresponding analog signal based on the first and the second clock signals, then generating a plurality of analog signals.

12. The data stream processing method of claim 11, wherein the combining rule is to chose two original digital data streams from a plurality of original digital data streams.

13. The data stream processing method of claim 11, wherein the first clock signal has a positive edge and a negative edge in each predetermined converting period.

14. The data stream processing method of claim 13, wherein the positive edge triggers converting the digital data of a corresponding first original digital data stream of each data streams.

15. The data stream processing method of claim 14, wherein the negative edge triggers converting the digital data of a corresponding second original digital data stream of each data streams.